



INFORMATION DISCLOSURE CITATION PTO-1449	Patent No. Docket No. 010704A Applicant(s): NAKATA, Shunji et al. Filing Date: April 27, 2004	Serial No. 10/709,287 Group Art Unit: 2816
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#### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
CJL	AA 5,936,455	Kobayashi et al.	08/10/1999	327	437	
	AB					
	AC					
	AD					
	AE					

#### FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
CJL	AF 0 901 230	03/10/1999	Europe	Yes
CJL	AG 0 851 323	07/01/1998	Europe	Yes
	AH			
	AI			
	AJ			

#### OTHER DOCUMENTS

CJL	AK	Kim et al., "A Low-Power Half-Swing Clocking Scheme for Flip-Flop with Complementary Gate and Source Drive", IEICE TRANS. ELECTRON., Vol. E82-C, No. 9, September 1999, pages 1777-1779.
CJL	AL	Copy of European Patent Office Communication with European Search Report for corresponding European Patent Application No. 01410063 dated February 4, 2005.

Examiner Cassandra Cof Date Considered April 13, 2005